

## WHAT IS CLAIMED IS:

1. A display device having at least a different resolution first mode and second mode having a lower resolution than said first mode, comprising:
  - 5 a pixel portion comprised of pixel circuits, for writing pixel data into pixel cells through switching elements, arranged so as to form a matrix of at least a plurality of rows;
  - 10 a plurality of scan lines arranged so as to correspond to a row arrangement of said pixel circuits and controlling conduction of said switching elements; at least one signal line arranged so as to correspond to a column arrangement of said pixel circuits and propagating said pixel data; and
  - 15 a vertical drive circuit for processing for successively scanning said scan lines in a row direction by scan pulses and successively selecting the pixel circuits connected to the scan lines in units of rows in said first mode and for processing for successively scanning said scan lines for every adjacent plurality of scan lines in the row direction by the scan pulses and successively selecting the pixel circuits connected to said plurality of scan lines in units of the plurality of rows in said second mode.
  - 20 25 2. A display device as set forth in claim 1, wherein said vertical drive circuit sets a rear edge

timing of the scan pulses for outputting the scan pulses to be output to a plurality of scan lines to be scanned simultaneously in parallel to the scan lines of a previous stage earlier than the rear edge timing of the 5 scan pulses to be output to the scan lines of the next stage in said second mode.

3. A display device as set forth in claim 1, further comprising a horizontal drive circuit including a selector having selector switches for selecting the pixel 10 data and supplying the same to said signal lines, said selector switches formed by connecting pluralities of switches in parallel to the corresponding signal lines, making said pluralities of switches conductive and outputting the selected pixel data to the signal lines 15 through said pluralities of switches in said first mode, and making any switches among said pluralities of switches conductive and outputting the selected pixel data to the signal lines through said switches in said second mode.

20 4. A display device as set forth in claim 2, further comprising a horizontal drive circuit including a selector having selector switches for selecting the pixel data and supplying the same to said signal lines, said selector switches formed by connecting pluralities of 25 switches in parallel to the corresponding signal lines, making said pluralities of switches conductive and

outputting the selected pixel data to the signal lines through said pluralities of switches in said first mode, and making any switches among said pluralities of switches conductive and outputting the selected pixel 5 data to the signal lines through said switches in said second mode.

5. A display device as set forth in claim 1, wherein said display device:

comprises a plurality of said signal lines  
10 and

comprises a plurality of horizontal drive circuits dividing said plurality of signal lines into a plurality of groups and supplying pixel data to the signal lines corresponding to the divided groups.

15 6. A display device as set forth in claim 1, wherein said display device:

comprises a plurality of said signal lines  
and

comprises a plurality of horizontal drive circuits dividing said plurality of signal lines into a plurality of groups and supplying pixel data to the signal lines corresponding to the divided groups,

each horizontal drive circuit including a selector having selector switches for selecting the pixel 25 data and supplying the same to said signal lines, said selector switches formed by connecting pluralities of

switches in parallel to the corresponding signal lines,  
making said pluralities of switches conductive and  
outputting the selected pixel data to the signal lines  
through said pluralities of switches in said first mode,  
5 and making any switches among said pluralities of  
switches conductive and outputting the selected pixel  
data to the signal lines through said switches in said  
second mode.

7. A display device as set forth in claim 2,  
10 wherein said display device:

comprises a plurality of said signal lines  
and  
comprises a plurality of horizontal drive  
circuits dividing said plurality of signal lines into a  
15 plurality of groups and supplying pixel data to the  
signal lines corresponding to the divided groups,  
each horizontal drive circuit including a  
selector having selector switches for selecting the pixel  
data and supplying the same to said signal lines, said  
20 selector switches formed by connecting pluralities of  
switches in parallel to the corresponding signal lines,  
making said pluralities of switches conductive and  
outputting the selected pixel data to the signal lines  
through said pluralities of switches in said first mode,  
25 and making any switches among said pluralities of  
switches conductive and outputting the selected pixel

data to the signal lines through said switches in said second mode.

8. A display device as set forth in claim 1,  
wherein said pixel cells are liquid crystal cells.

5 9. A method of driving a display device  
including a pixel portion comprised of pixel circuits,  
for writing pixel data into pixel cells through switching  
elements, arranged so as to form a matrix of at least a  
plurality of rows and a plurality of scan lines arranged  
10 so as to correspond to the row arrangement of said pixel  
circuits and for controlling the conduction of said  
switching elements, comprising the steps of

processing for successively scanning said  
scan lines in the row direction by scan pulses and  
15 successively selecting the pixel circuits connected to  
the scan lines in units of rows in a first mode having a  
predetermined resolution and

processing for successively scanning said  
scan lines for every adjacent plurality of scan lines in  
20 the row direction by the scan pulses and successively  
selecting the pixel circuits connected to said plurality  
of scan lines in units of said plurality of rows in a  
second mode having a lower resolution than said first  
mode.

25 10. A method of driving a display device as set  
forth in claim 9, further comprising setting a rear edge

timing of the scan pulses for outputting the scan pulses to be output to a plurality of scan lines to be scanned simultaneously in parallel to the scan lines of a previous stage earlier than the rear edge timing of the 5 scan pulses to be output to the scan lines of the next stage in said second mode.

11. A method of driving a display device as set forth in claim 9, wherein said pixel cells are liquid crystal cells.